ABSTRACT

The objective is to provide a data transfer control device and electronic equipment that make it possible to utilize the resources possessed by each node efficiently, thus reducing processing overheads. A packet shaping circuit receives a self-ID packet conforming to the IEEE 1394 standard from each of the nodes, shapes a packet having a frame made of a single header and data that is formed from an assembled series of self-ID packets, and interfaces with an upper layer. Parity information is erased from each self-ID packet, data of the packet is formed from these assembled self-ID packets without the parity information, and also error status information is appended to a trailer of parity information. The header of the packet is written to a header area and the data thereof is written to a data area, and also a data pointer indicating the address of the data is appended to the header of the packet. An area that is dedicated to self-ID packets is provided in the data area. The data transfer control device detects whether or not the current period is a self-ID period, and regards packets that are sent in during that self-ID period as self-ID packets, for performing packet shaping.